

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Tan et al.

Serial No.: 10/645,389

Filed: August 21, 2003

For: METHODS RELATING TO THE

RECONSTRUCTION OF

SEMICONDUCTOR WAFERS FOR WAFER-LEVEL PROCESSING (as

amended)

Confirmation No.: 8099

Examiner: W. Brewster

Group Art Unit: 2823

Attorney Docket No.: 2269-5528US

(02-1052.00/US)

Notice of Allowance Mailed:

December 30, 2005

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AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Abstract appear on page 17 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 18 of this paper.

Amendments to the Drawings appear on page 25 of this paper and include both attached replacement sheets and annotated sheets showing changes.

Remarks begin on page 26 of this paper.